



Title: QUANTIZER FOR A SIGMA DELTA MODULATOR, AND SIGMA DELTA MODULATOR
Applicant: Doerr et al.
Atty Docket: 1406/176

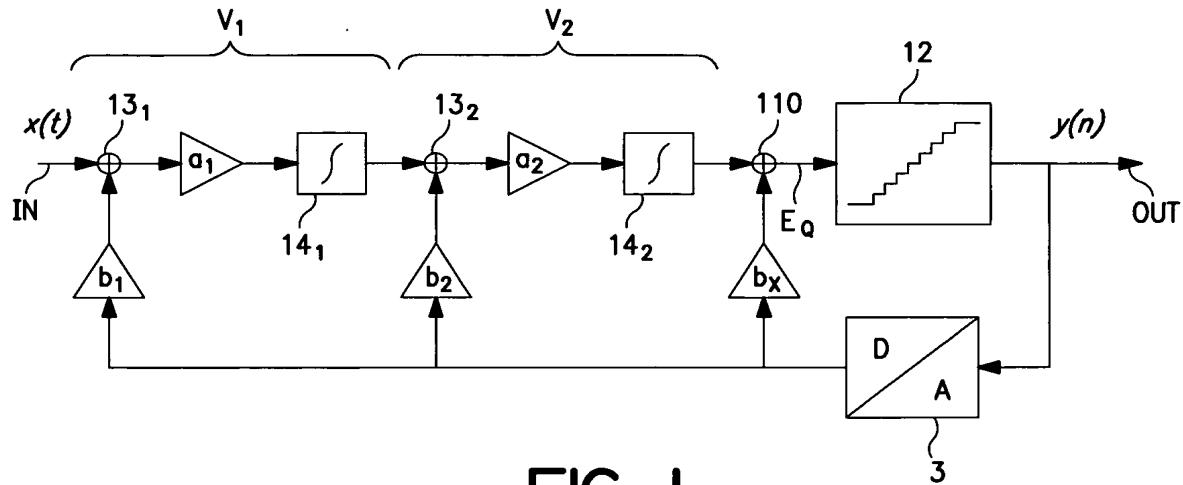


FIG. 1

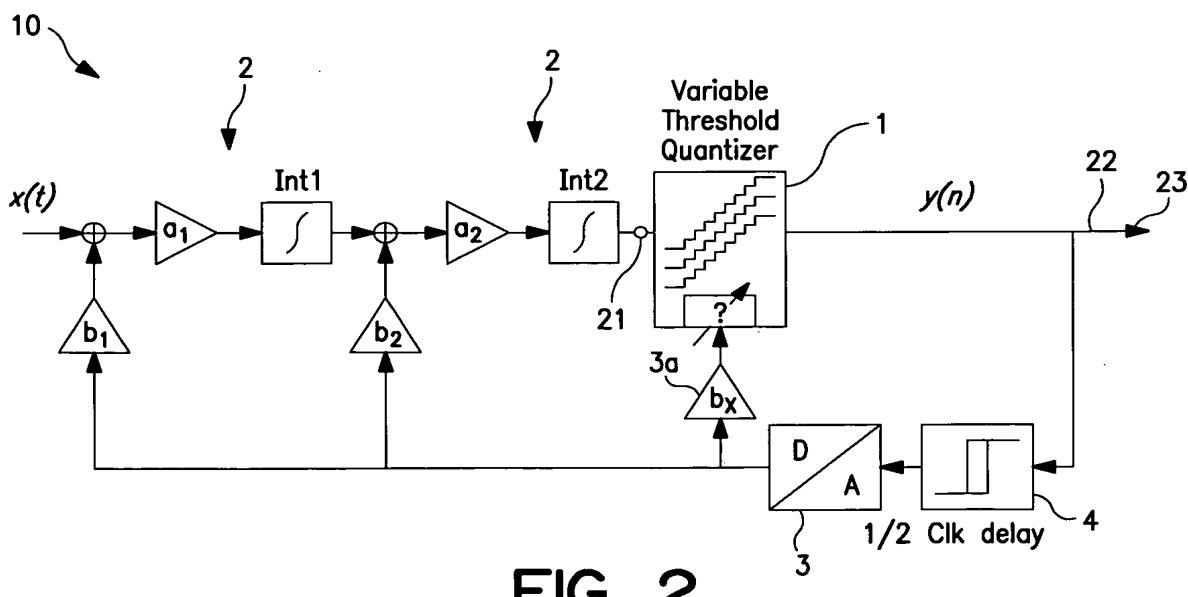


FIG. 2

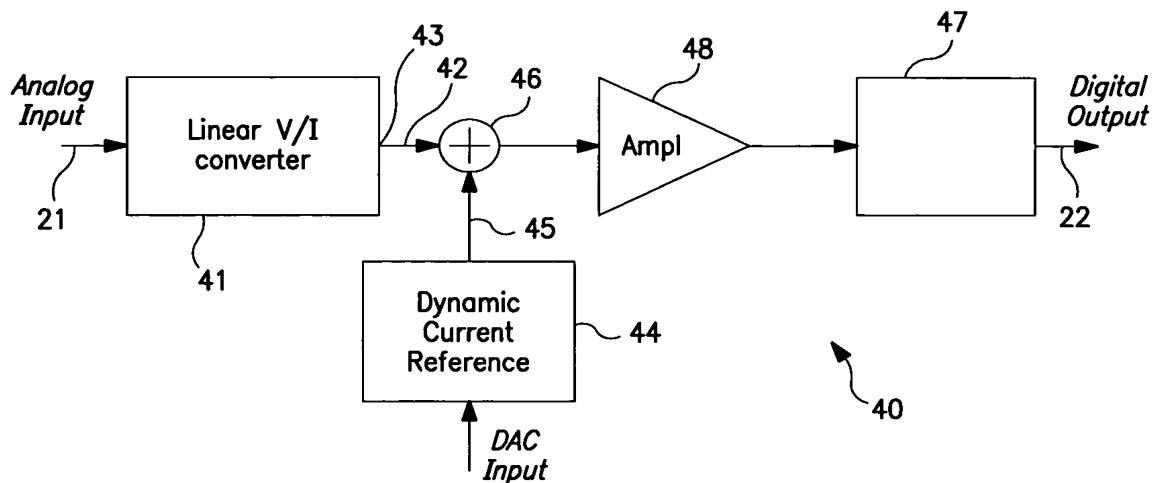


FIG. 3

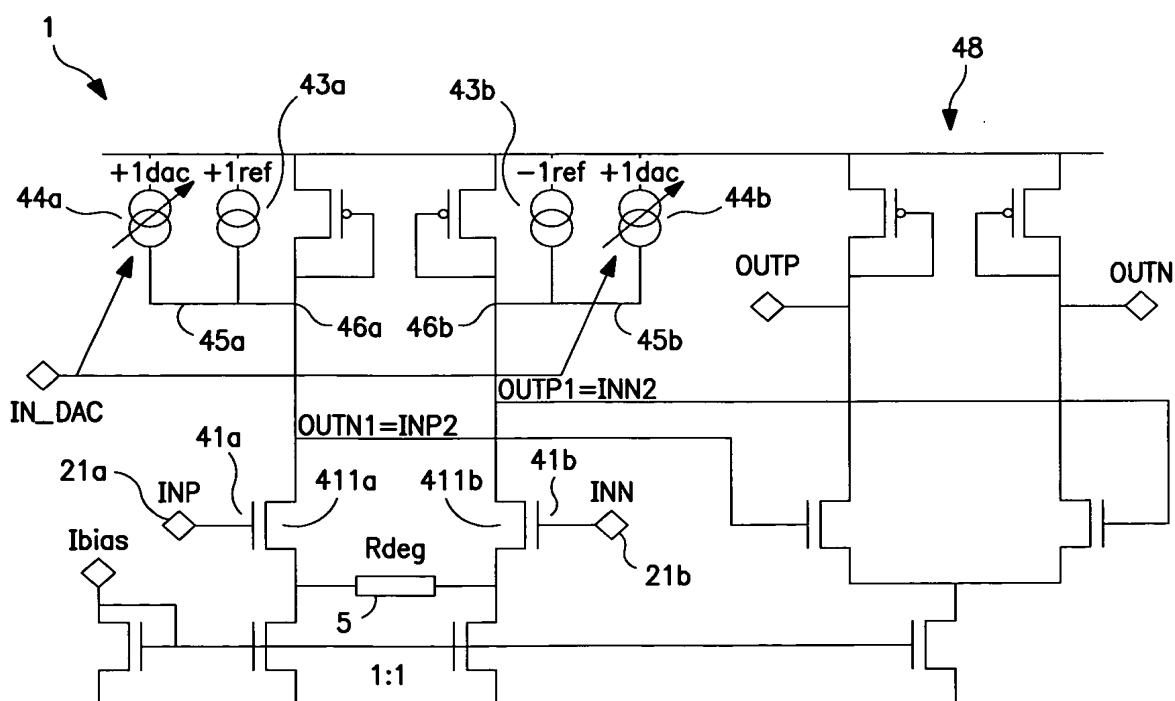


FIG. 4

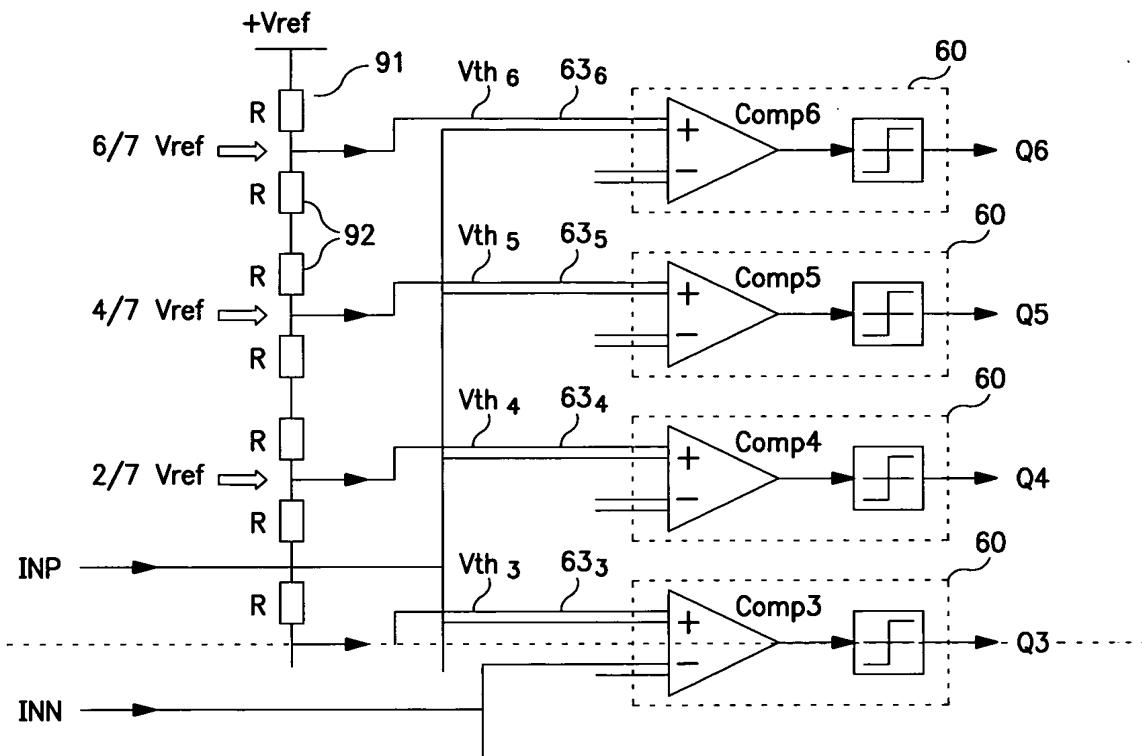


FIG. 5

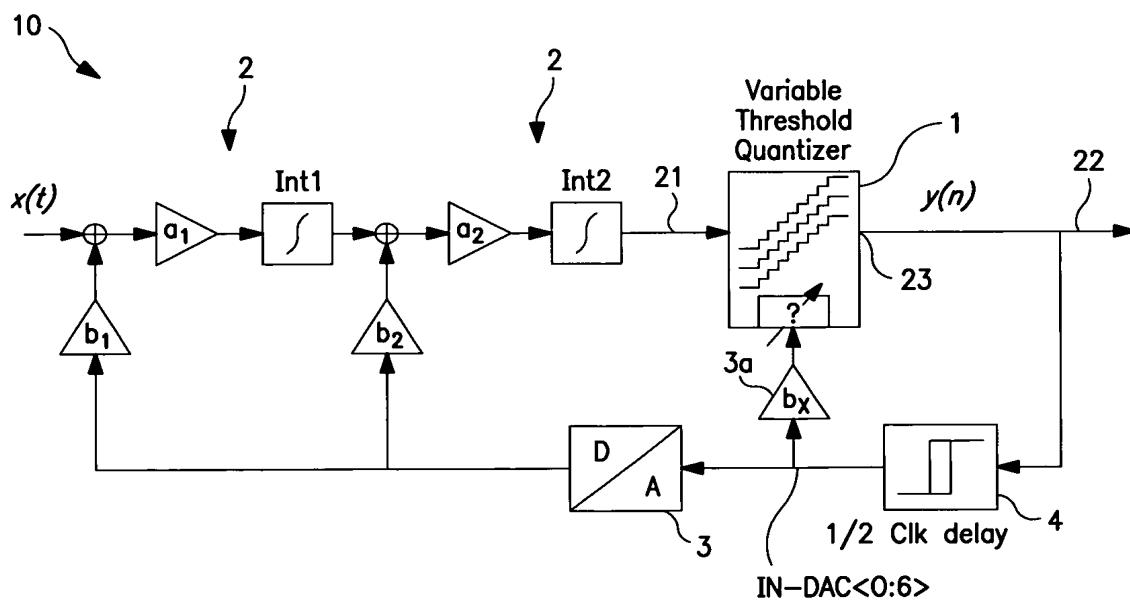


FIG. 5a

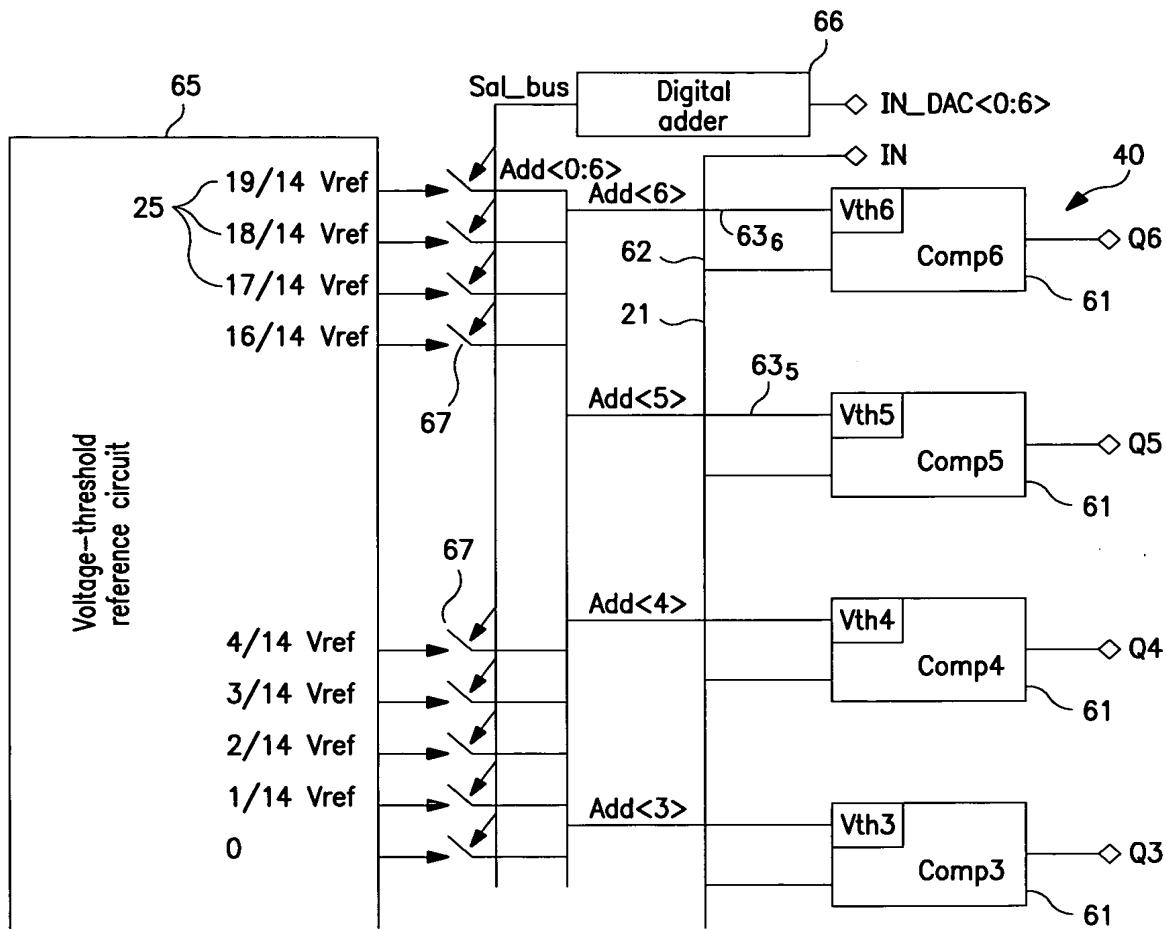


FIG. 6

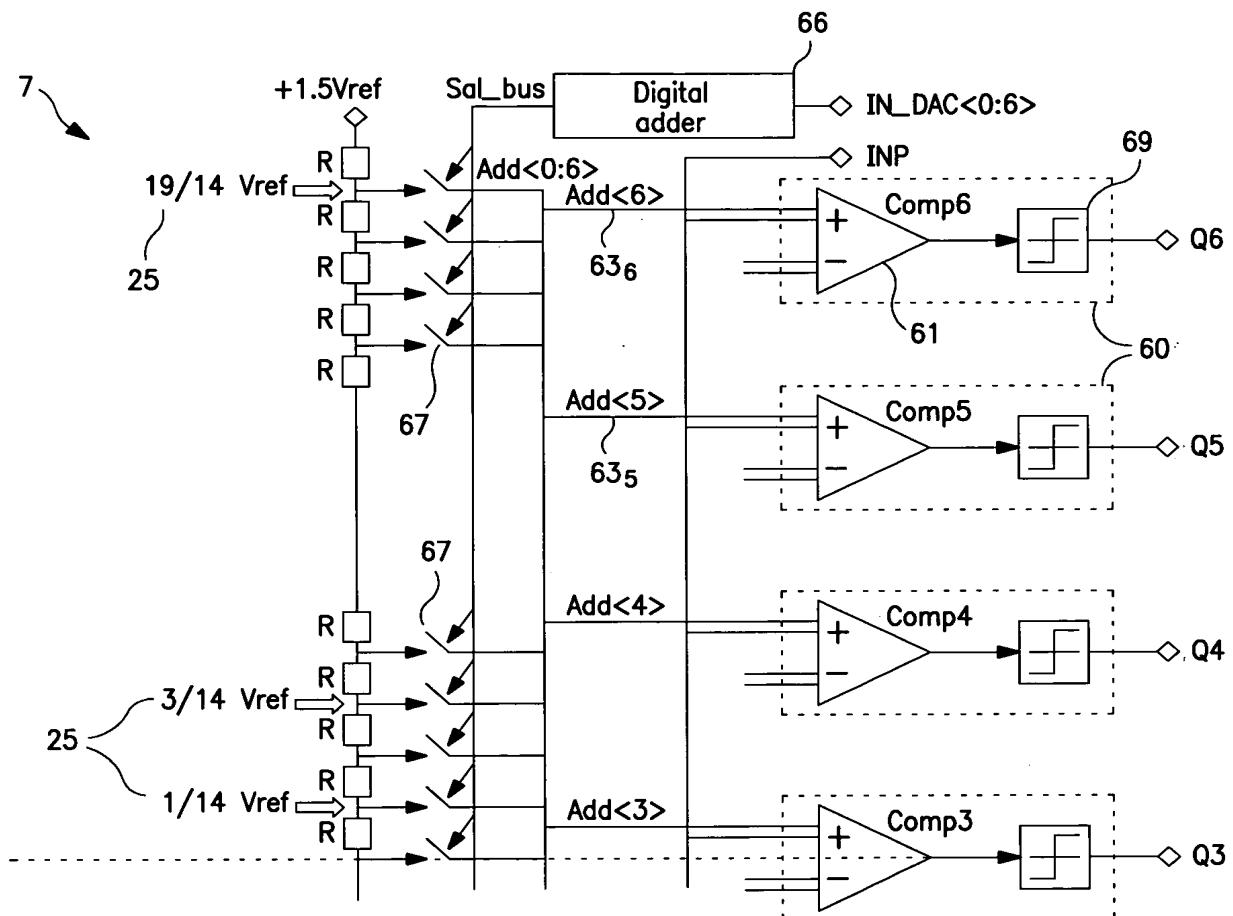


FIG. 7

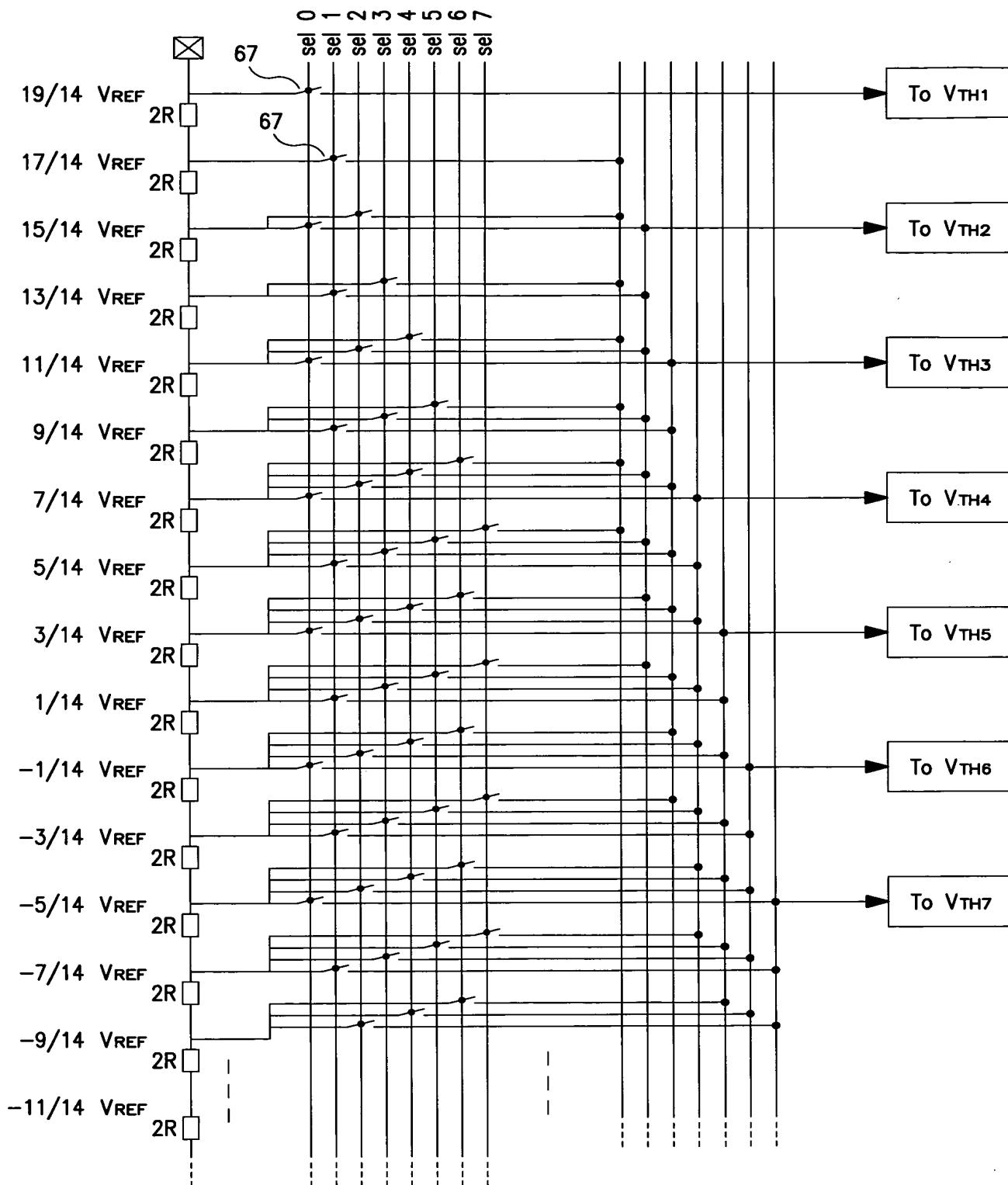


FIG. 8

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b3=1/2

IN_DAC<0:6>	dat	sel7	sel6	sel5	sel4	sel3	sel2	sel1	sel0
=0000000	0	1	0	0	0	0	0	0	0
=0000001	1	0	1	0	0	0	0	0	0
=0000011	2	0	0	1	0	0	0	0	0
=0000111	3	0	0	0	1	0	0	0	0
=0001111	4	0	0	0	0	1	0	0	0
=0011111	5	0	0	0	0	0	1	0	0
=0111111	6	0	0	0	0	0	0	1	0
=1111111	7	0	0	0	0	0	0	0	1

b3=1/2

	NOM (if b3=0)	dat=0	dat=1	dat=2	dat=3	dat=4	dat=5	dat=6	dat=7
Vth1	+6/7	+5/14	+7/14	+9/14	+11/14	+13/14	+15/14	+17/14	+19/14
Vth2	+4/7	+1/14	+3/14	+5/14	+7/14	+9/14	+11/14	+13/14	+15/14
Vth3	+2/7	-3/14	-1/14	+1/14	+3/14	+5/14	+7/14	+9/14	+11/14
Vth4	0	-7/14	-5/14	-3/14	-1/14	+1/14	+3/14	+5/14	+7/14
Vth5	-2/7	-11/14	-9/14	-7/14	-5/14	-3/14	-1/14	+1/14	+3/14
Vth6	-4/7	-15/14	-13/14	-11/14	-9/14	-7/14	-5/14	-3/14	-1/14
Vth7	-6/7	-19/14	-17/14	-15/14	-13/14	-11/14	-9/14	-7/14	-5/14

FIG. 9

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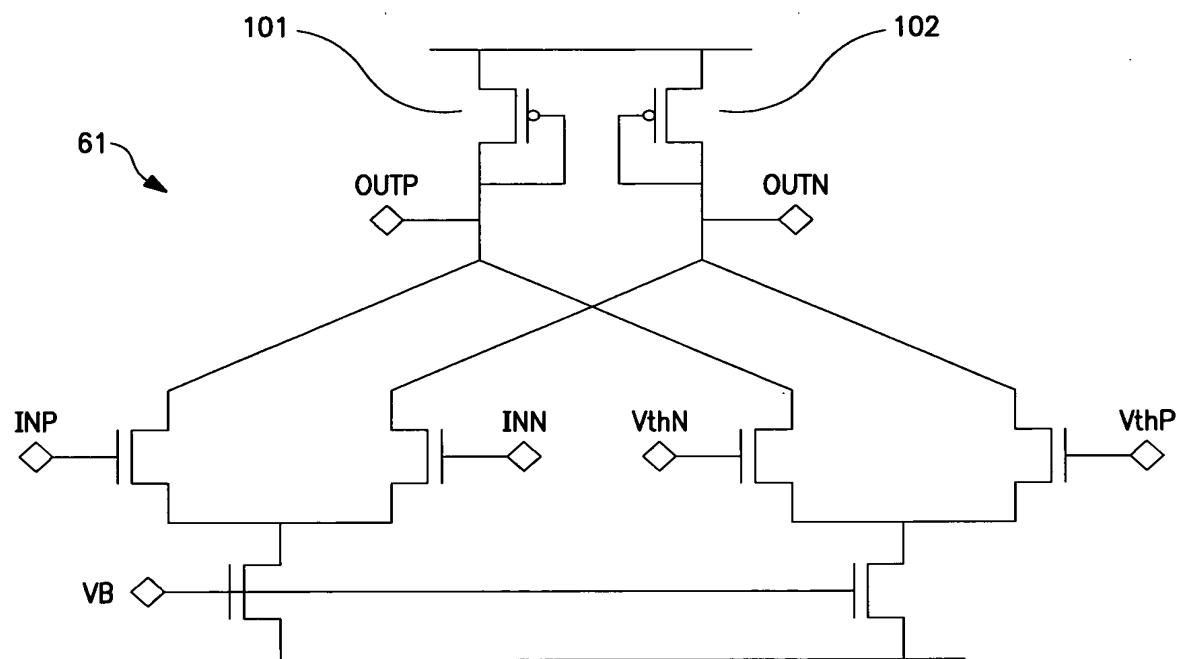


FIG. 10